SRI VENKATESWARA

COLLEGE OF ENGINEERING AND TECHNOLOGY

Thirupachur-631203, Tiruvallur TK & DT

Approved by AICTE New Delhi & Affiliated to Anna University, Chennai

(A Telugu Minority Institution)

ACADEMIC YEAR

2018-2019

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Faculty Development Program on Discrete Time Signal Processing

REGISTRATION FORM

Name   :
Designation   :
Department   :
Institution   :
Experience   :
Mobile No.   :
Email.   :

DECLARATION

The information Provided by me is true to the best of my knowledge .I agree to abide by the rules and regulations governing the Faculty Development Program. If selected, I shall attend the program for the entire duration.

Date   Signature of the Participant

BONAFIDE CERTIFICATE

Mr./Mrs _____ is an employee of our institution and permitted to attend the FDP , if selected.

Date   Signature of the
                Head of the Institution

REGISTRATION PROCESS:

Registration for the FDP can be done using the attached form or photo copy of the form. The completed form duly signed by Head of the Institution should be sent by post to the Coodinator or the scanned copy of the same can be mailed to fdp.ece@svcet.com.

Registration link:
https://forms.gle/dcsfsXfXbDv1V4HuU9
* No Registration fees.
* Total number of participants is limited to Thirty-Five only.
* Participants will be selected on first come first served basis.
* Selected candidates will be intimated by e-mail. Confirmation of participation is to be made by email.
* No TA/DA will be paid
* Participation certificates will be issued to the candidates who attend the program in full, by the institutions.

Topics to be covered
* Discrete Time System analysis
* Discrete Fourier Transform & Computation
* Design of Digital Filters
* Digital Signal Processors

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FACULTY DEVELOPMENT PROGRAM ON DISCRETE TIME SIGNAL PROCESSING

21.08.2018 – 28.08.2018

ORGANISED BY
THE DEPARTMENT OF
ELECTRONICS &
COMMUNICATION ENGINEERING

ABOUT THE COLLEGE

Sri Venkateswara College of Engineering and Technology (SVCET) is an engineering college located in Thiruppachur, Tiruvallur, a district adjacent to Chennai city (formerly Madras), in Southern India.
The college was established in 1999 with AICTE approval and started functioning in 2000.
Sri Venkateswara College of Educational and Cultural trust which runs this college is committed to provide quality education in building the future of today's youth. In the era of globalization, working environment has become highly competitive and challenging.
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A Fine Arts Club at the college aims at developing the student's talents in music , drama, drawing, writing and elocution

Eligible students can avail scholarship facilities offered by both the Government of India and the Government of Tamil Nadu. Beside these ,the college management may also offer financial supporting deserving cases

ABOUT DEPARTMENT

The Department of Electronics and Communication Engineering is one of the major Departments of the institute. The department is housed in a newly built building having modern amenities. The laboratories are well equipped with the latest software and hardware tools to give a practical hand to the students. A Seminar Hall, fully equipped with audio-visual aids and with the seating capacity of 120 students has been facilitated in the department. At times this department organizes the Guest Lectures for the students to bridge the gap between Industry and Academics.

ABOUT FDP PROGRAM

Discrete Time Signal Processing is concerned with the representation, transformation and manipulation of signals and the information they contain. Signal Processing has developed rapidly over the past few decades and has numerous applications in the field of Audio and Speech Processing, Radar, Sonar, Robotics, Big data, Bio medical and many more. In this course, we begin with the review of Signals and Systems including the summary of analysis and synthesis equation for Fourier Transforms tools and z-transform, An in-depth computation of DFT and FFT algorithms in spectral analysis and filtering applications is presented. This course develops different methods of filter design in both IIR and FIR and the corresponding structures.

TARGET AUDIENCE
Faculty members of all departments of Anna university affiliated colleges.

RESOURCE PERSONS
Resource persons from repudiated Academic institutions and industries will deliver the sessions and handles the practical sessions

ORGANISING COMMITTEE

Chair:
Dr. S. K. Purushothaman, Chairman, Sri Venkateswara College of Engineering&Technology
Mr. G. Sreekanth, Secretary, Sri Venkateswara College of Engineering&Technology

Patrons:
Dr. S. Palani, Principal Sri Venkateswara College of Engineering&Technology

CO-ORDINATORS:
Mrs.S.Geetha, Assistant Professor / EEE
Mr.D.Siva Kumar, Assistant Professor / EEE

Important Dates
Submission of Application   : 18.08.2018
Intimation of selection   : 19.08.2018
Confirmation by Participants : 20.08.2018

Contact Details :
Mr. P. G. Baskar , Coordinator
Mail id : fdp.ece@svcet.com.

PRINCIPAL
Sri Venkateswara College of
Engineering and Technology,
Thirupachur, Thiruvallur - 631 203.

SRI VENKATESWARA COLLEGE OF ENGG AND TECH.
Thirupachur
Pin : 631 203

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	Academic year : 2018 - 2019
NAME	Faculty Development Program on Discrete Time Signal Processing
DATE OF ACTIVITY/EVENT	23.08.2018-27.08.2018
VENUE	N-Block Conference Hall, SVCET- Thirupachur, Thiruvallur.
ACTIVITY / EVENT OBJECTIVES	1. To provide a comprehensive treatment of the important issues in design, implementation, and application of digital signal processing algorithms. 2. To provide the student with a broad, yet strong background in techniques for processing digital signals
DESCRIPTION OF THE EVENT	• This course covers fundamental digital signal processing (DSP) techniques that form the basis to a wide variety of application areas. • Topics include discrete-time signals and systems, time domain analysis • Solutions of difference equations, Z-transform analysis, discrete Fourier transforms (DFT), sampling theorem, and Fourier analysis of linear time-invariant systems • The course also analyzes the broader impacts of DSP in a global, economic and societal context.
OUTCOMES OF THE EVENT	1. Know basic concepts of discrete-time signals and systems. Be able to analyze discrete-time systems by solving different equations. 2. Understand the sampling theory and know techniques for processing continuous-time signals using digital systems. 3. Be able to analyze linear time-invariant systems in frequency domain. 4. Describe the use and development of a given technology as a human and cultured activity
EVENT COORDINATOR	Mr.P.G.Bhaskar, Coordinator
ORGANISED BY	Department of Electronics and Communication Engineering
NUMBER OF PARTICIPANTS	35
PHOTO	

FEEDBACK

1 (Poor)	2 (Average)	3 (Good)	4 (Excellent)
4	11	9	11

Signature of the coordinator/convenor	

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TRAINING PROGRAMME FOR NON-TEACHING FACULTY MEMBERS ON PROFESSIONAL DEVELOPMENT

Date : 06.05.2019 - 10.05.2019

Speakers

Mrs.B.Sowntharya /MBA/ SVCET

Mr. D.Manikandan / S&H / SVCET

Dr.P.G.BHASKAR	**Dr.J.Venkatesh**
HOD MECH	**PRINCIPAL**
ORGANISING SECRETARY	**ORGANISING CHAIRPERSON**

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Academic year : 2018 - 2019	
NAME	Training Programme on Professional Development For Non-Teaching Staff
DATE OF ACTIVITY/EVENT	06.05.2019-10.05.2019
VENUE	F-Block Function Hall, SVCET- Thirupachur, Thiruvallur.
ACTIVITY / EVENT OBJECTIVES	• To improve skills and knowledge in order to raise the level of job performance. • To leverage strengths more fully in your job in support of your department's goals and the mission of the college. • To learn new skills and knowledge that will prepare you for new responsibilities or increase your career advancement potential
DESCRIPTION OF THE EVENT	1. Decide on the number of training objectives 2. Craft objectives based on employee skill level 3. Training objectives need KPIs 4. Use action verbs to correlate success 5. Analyze your training objectives
OUTCOMES OF THE EVENT	1. Develop a new skill set. 2. Develop your workplace skills. 3. Take up leadership responsibilities. 4. Expand your professional network. 5. Level-up your credentials.
EVENT COORDINATOR	Dr.P.G.Bhaskar, Coordinator
ORGANISED BY	Department of Master of Business Administration
NUMBER OF PARTICIPANTS	28
PHOTO	**BROUCHURE:**

FEEDBACK			
1 (Poor)	2 (Average)	3 (Good)	4 (Excellent)
5	6	9	5

Signature of the coordinator/convenor	

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Thirupachur, Thiruvallur - 631 203.