



SRI VENKATESWARA

COLLEGE OF ENGINEERING AND TECHNOLOGY

Thirupachur-631203, Tiruvallur TK & DT
Approved by AICTE New Delhi & Affiliated to Anna University, Chennai
(A Telugu Minority Institution)

1.3.2 Number of Student Undertaking Projects/mini project / Internship for the Academic Year 2022-23.

S.NO	PROGRAM NAME	PROGRAM CODE	REG.NO.	STUDENT NAME	YEAR	PROJECT WORK	MINI PROJECT
1.	M.E VLSI	419	112421419017	SUMITHRA R	II	✓	
2.	M.E VLSI	419	112421419007	PRIYADHARSHINI R	II	✓	
3.	M.E VLSI	419	112421419010	SARANYA V	II	✓	
4.	M.E VLSI	419	112421419006	PREETHA A	II	✓	
5.	M.E VLSI	419	112421419018	VAIRAMUTHU G	II	✓	
6.	M.E VLSI	419	112421419016	SUMAN M	II	✓	
7.	M.E VLSI	419	112421419001	JANANIG	II	✓	
8.	M.E VLSI	419	112421419011	SATHYARAJ R	II	✓	
9.	M.E VLSI	419	112421419002	KANIMOZHI J	II	✓	
10.	M.E VLSI	419	112421419005	NARMADHA S	II	✓	



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List of Students Under taking Project /Work For the Academic Year 2022-2023

Program Name: VLSI DESIGN

PROJECT BATCH LIST 2022-2023

BATCH NUMBER	REGISTER NUMBER	STUDENTS NAME	PROJECT TITLE	NAME OF THE GUIDE
1	112421419017	SUMITHRA R	THROUGHPUT/OPTIMIZED ARCHITECTURE FOR ELLIPTIC-CURVE DIFFIE-HELLEMAN PROTOCOL	Mr. V. - RAJESHKUMAR
2	112421419007	PRIYADHARSHINI R	ENERGY EFFICIENT ECG SIGNAL PROCESSING USING PRUNED AND TRUNCATED HAAR DISCRTE WAVELET TRANSFORM & INTEGER WAVELET TRANSFORM	Mr. V. RAJESHKUMAR
3	112421419010	SARANYA V	A FULLY DIFFERENTIAL DIFFERENCE TRANSDUCTANCE AMPLIFIER TOPOLOGY BASED CMOS INVERTERS	Mr.E.MURALI
4	112421419006	PREETHA A	VLSI IMPLIMENTATION OF HIGH ADAPTVE FILTER STRUCTURE FOR SPEECH ENHANCEMENT	Mr. V. RAJESHKUMAR
5	112421419018	VAIRAMUTHU G	HIGH SPEED AREA EFFICIENT VLSI ARCHITECTURE OF THREE-OPERAND BINARY ADDER	Mr. V. RAJESHKUMAR
6	112421419016	SUMAN M	AN EFFICIENT VLSI IMPLIMENTATION OF EDGE DETECTION	Mr. V. RAJESHKUMAR



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			ALGORITHM FOR IMAGE PROCESSING APPLICATION	
7	112421419001	JANANIG	IMPLIMENTATION OF LAUNCHING WEBPAGE AND TAKING SCREENSHOT USING SELINIUM	Mr. V. RAJESHKUMAR
8	112421419011	SATHYARAJ R	A LOW POWER AND HIGH-ACCURACY APPROXIMATE MULTIPLIER WITH RECONFIGURABLE TRUNCATION	Mr. V. RAJESHKUMAR
9	112421419002	KANIMOZHI J	DESIGN OF A LOW POWER AND LOW AREA FLASH ADC USING SAR COMPARATOR ON CMOS PROCESS	Mr. V. RAJESHKUMAR
10	112421419005	NARMADHA S	IMPLEMENTATION OF MODULATOR AND DEMODULATOR FOR 5G	Mr. V. RAJESHKUMAR




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