



SRI VENKATESWARA

COLLEGE OF ENGINEERING AND TECHNOLOGY

Thirupachur-631203, Tiruvallur TK & DT

Approved by AICTE New Delhi & Affiliated to Anna University, Chennai
(A Telugu Minority Institution)

18ECEVAC02 - "FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"

EVENT REPORT

Name of the Course: "Fuzzy Logic for Controlling the Electronics Devices"

Name of the Instructor: Mr.B. RaviBabu, Asso.Prof , SIET

Year and Branch: III, IV & ME-VLSI - ELECTRONICS AND COMMUNICATION ENGINEERING

Duration of Course: 32 Hrs (18.02.2019 to 23.02.2019)

SUMMARY OF THE EVENT:

The Course was inaugurated on **18.02.2019** at 8.30.00 am by our respectable Principal and HOD. As per the Course plan session was started and went smooth for the Remaining days. On the last day **23.02.2019** by 3.30 pm the course was Completed Successfully.

During the Six Days of the program Students were participated with enthusiasm and they finally gained Knowledge on "Fuzzy Logic for Controlling the Electronics Devices"

ASSESMENT MODE:

No of Students Enrolled: 88

Scheme of Exam: Offline

No of Students appeared: 88

No of Students Passed: 88

Date of Exam: 23.02.2019

OUTCOME:

This course has helped the students to have Fuzzy tolerance, and how to assign the values. At the end of the course the application of Fuzzy in Artificial Intelligence and Robotics.

COORDINATOR

(Mr. Anand Kumar)

HOD/ECE

(Mr.V. Rajeshkumar)



PRINCIPAL

Sri Venkateswara College of
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Thirupachur, Tiruvallur - 631 203



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18ECEVAC02 - "FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"



Value Added Course on "Fuzzy Logic for Controlling the Electronics Devices" handled by Mr.B.

RaviBabu, Asso. Prof SIET

PRINCIPAL

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REQUEST LETTER

From

Mr. V. Rajeshkumar
HOD /ECE
SVCET
Thirupachur.

Permitted
Jy

To,

The Principal
SVCET
Thirupachur

Respected sir,

Sub: Requesting permission to conduct a 6 Days Value added course –Reg

As the Head of **Electronics and Communication Engineering** Department, I would like to request your support and permission to conduct Training Program on “**Fuzzy Logic for Controlling the Electronics Devices**” in our department. There is a need that has been identified of keeping the students up to date with the new technologies. Most students would not have knowledge on this area which is not available in our curriculum. So, there is need for a there is need for this program to challenge the It and industries opportunities.

The dates for the Training program have been fixed. The Training will start on **18.02.18** and end on **23.02.18** (32Hrs). Will be handled by **Mr. B. RaviBabu, Associate Professor**. I kindly request your assistance in providing Tea and snack for the participants (students).

Please find the attached Invitation, circular to students, and a presentation highlighting the key areas that will be focused in the Training program.

Thanking You

HOD/ECE

(Mr.V. Rajeshkumar)

PRINCIPAL

(Dr.J. Venkatesh)



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12.02.2019

CIRCULAR

The department of Electronics and Communication Engineering will be Value added course on "Fuzzy Logic for Controlling the Electronics Devices" from 18.02.2019 to 23.02.2019 (32 Hrs). It is proposed to keep the students up to date with the new technologies.

All the students are requested to participate actively, certificate will be provided for the enrolled Participants.

All the interested candidates should enroll their names to Mr. Anand Kumar, Assistant Professor, Department on "Fuzzy Logic for Controlling the Electronics Devices" of Electronics and Communication Engineering on or before 15.02.2019



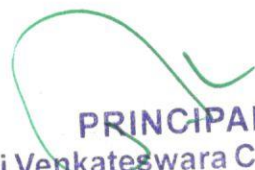
HOD/ECE

(Mr.V. Rajeshkumar)

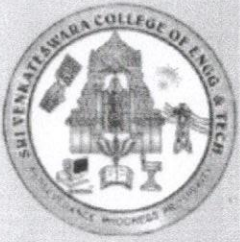


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(Dr.J. Venkatesh)



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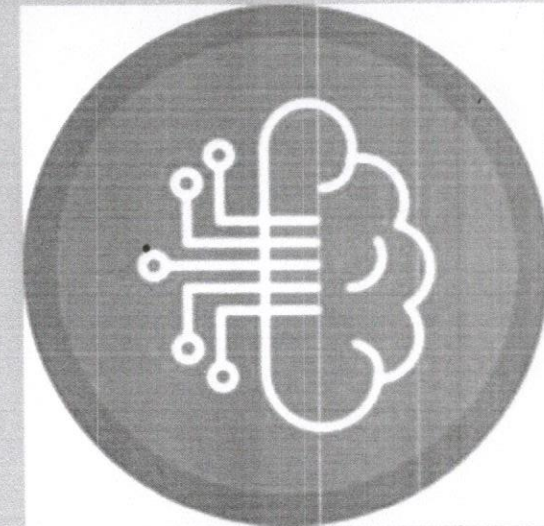
APPROVED by AICTE, New Delhi and Affiliated to Anna University , Chennai,

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
"FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"

Date : 18.02.2019 - 23.02.2019

RESOURCE PERSON

1.Mr.B.RaviBabu,Associate Professor,ECE,Sidhart
Institute of Engineering and Technology



Mr. V. Rajeshkumar

HOD / ECE

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Dr.J. Venkatesh

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18ECEVAC02 - FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES
RESOURCE PERSON PROFILE

Name : Mr. B. RaviBabu
Designation : Associate Professor
Department : Electronics and Communication Engineering
Organization : Sidhart Institute of Engineering and Technology
Phone Number : 9515708299
Email.ID : bathalaravibabu@gmail.com
Qualification : ME
Experience : 13 Years




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18ECEVAC02 - "FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"

COURSE CONTENT

Objective

1. To develop the fundamental concepts such as fuzzy sets, operations and fuzzy relations.
2. To learn about the fuzzification of scalar variables and the defuzzification of membership functions

Module 1: Fuzzy relations

Cartesian product - crisp relations- cardinality - operations and properties of crisp relations. Fuzzy relations- cardinality - operations - properties of fuzzy relations - fuzzy Cartesian product and composition - Fuzzy tolerance and equivalence relations - value assignments and other format of the composition operation.

Module 2: Fuzzification and Defuzzification

Features of the membership functions - various forms - fuzzification - defuzzification to crisp sets - cuts for fuzzy relations - Defuzzification to scalars -Fuzzy logic and approximate reasoning - Other forms of the implication operation.

Module 3: Fuzzy Systems

Natural language - Linguistic hedges - Fuzzy (Rule based) System - Aggregation of fuzzy rules - Graphical techniques of inference - Membership value assignments - Intuition Inference- rank ordering - Fuzzy Associative memories

Outcomes:

1. Understand the basic ideas of fuzzy sets, operations and properties of fuzzy sets and also about fuzzy relations.
2. Understand the basic features of membership functions, fuzzification process and defuzzification process.
3. Design fuzzy rule-based system.


COORDINATOR

(Mr. Anand Kumar)




HOD/ECE

(Mr. V. Rajeshkumar)


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18ECEVAC02 - "FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"

COURSE SCHEDULE

6 Days Training Schedule – "Fuzzy Logic for Controlling the Electronics Devices"			
Day & Date	Session & Time	Speaker Name	Session Topics
Day 1 & 18.02.2019	8.30 am to 10.30am	Mr.B. RaviBabu, Asso. Prof SIET	Cartesian product crisp relations cardinality
	10.45 am to 12.45 pm	Mr.B. RaviBabu, Asso. Prof SIET	Operations and properties of crisp relations. Fuzzy relations-cardinality
	1.30 pm to 3.30pm	Mr.B.RaviBabu,Asso.Prof SIET	Operations properties of fuzzy relations fuzzy Cartesian
Day 2 & 19.02.2019	8.30 am to 10.30am	Mr.B. RaviBabu, Asso. Prof SIET	Product and composition Fuzzy tolerance
	10.45 am to 12.45 pm	Mr.B.RaviBabu,Asso.Prof SIET	Equivalence relations value assignments and other format of the composition operation
	1.30 pm to 3.30pm	Mr.B.RaviBabu,Asso.Prof SIET	Composition operation continue....
Day 3 & 20.02.2019	8.30 am to 10.30am	Mr.B.RaviBabu,Asso.Prof SIET	Features of the membership functions
	10.45 am to 12.45 pm	Mr.B.RaviBabu,Asso.Prof SIET	Various forms fuzzification
	1.30 pm to 3.30pm	Mr.B.RaviBabu,Asso.Prof SIET	Defuzzification to crisp Sets
Day 4& 21.02.2019	8.30 am to 10.30am	Mr.B.RaviBabu,Asso.Prof SIET	Cuts for fuzzy relations Defuzzification to scalars
	10.45 am to 12.45 pm	Mr.B.RaviBabu,Asso.Prof SIET	Fuzzy logic and approximate Reasoning Other forms of the implication operation.
Day5 & 22.02.2019	8.30 am to 10.30am	Mr.B.RaviBabu,Asso.Prof SIET	Natural language Linguistic hedges Fuzzy (Rule based) System
	10.45 am to 12.45 pm	Mr.B.RaviBabu,Asso.Prof SIET	Aggregation of fuzzy rules
Day6 & 23.02.2019	8.30 am to 10.30am	Mr.B.RaviBabu,Asso.Prof SIET	Graphical techniques of inference
	10.45 am to 12.45 pm	Mr.B.RaviBabu,Asso.Prof SIET	Membership value assignments intuition inference
	1.30 pm to 3.30pm	Mr.B.RaviBabu,Asso.Prof SIET	Rank ordering Fuzzy Associative memories & Test


COORDINATOR
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Course Code	18ECEVAC02
Name of the Course	FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES
Duration of the Course	32 HOURS
Academic Year	2018-19
Period of the Course	18.02.2019 TO 23.02.2019

LIST OF STUDENTS ENROLLED

S.NO	YEAR	REGISTER NUMBER	NAME OF STUDENTS	SIGNATURE
1	III	112416106001	ARAVIND J	J. Aravind
2	III	112416106002	ARUNODHAYA M	M. Arunodhaya
3	III	112416106003	ARUNTHATHI G	G. Arunthathi
4	III	112416106004	BALAJI S	S. Balaji
5	III	112416106005	DEEPAK KUMAR M	M. Deepak
6	III	112416106006	DILLIRANI M	M. Dillirani
7	III	112416106007	EASWARI M	M. Easwari
8	III	112416106008	GAYATHRI V	V. Gayathri
9	III	112416106009	GEETHA D	D. Geetha
10	III	112416106010	GOMATHI D	D. Gomathi
11	III	112416106011	GREESHMA MOHAN	Mohan Greeshma
12	III	112416106012	JAIGANESH V	V. Jaiganesh
13	III	112416106014	KALAIVANI S	S. Kalaivani
14	III	112416106015	KALAIVARMAN D	D. Kalaivarman
15	III	112416106016	KEERTHIKA G	G. Keerthika
16	III	112416106018	MOURRIYA HARRISH K	K. Mourriya
17	III	112416106019	NAVEEN KUMAR G	G. Naveen
18	III	112416106021	PRIYA DHARSHINI G	G. Priya
19	III	112416106022	RAKESH A	A. Rakesh
20	III	112416106023	RAMYA N	N. Ramya
21	III	112416106024	RAMYA S	S. Ramya
22	III	112416106025	SANDHIYA P S	S. Sandhya
23	III	112416106026	SANDHIYA S	S. Sandhya
24	III	112416106027	SANGAMITHRA V	V. Sangamithra
25	III	112416106029	SATHYA E	E. Sathya
26	III	112416106030	SENTHAMIZH SELVI R	R. Senthamizh
27	III	112416106033	SHARAN M C	C. Sharan
28	III	112416106034	SHATHIRKA R	R. Shathirka



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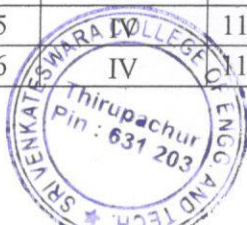


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S.NO	YEAR	REGISTER NUMBER	NAME OF STUDENTS	SIGNATURE
29	III	112416106035	SHOBANA G	G. Shobana
30	III	112416106036	SHOBANA M	M. Shobana
31	III	112416106037	SRIDHAR S	S. Sridhar
32	III	112416106038	SRINIVASAN M	M. Srinivasan
33	III	112416106039	SUMAN M	M. Suman
34	III	112416106040	SWAPNA R	R. Swapna
35	III	112416106041	SWATHI S	S. Swathi
36	III	112416106042	VANMATHI M	M. Vanmathi
37	III	112416106043	VARALAKSHMI A	A. Varalakshmi
38	III	112416106044	VARSHA A	Varsha A
39	III	112416106045	VELU M	M. Velu
40	III	112416106046	VINODHINI V	V. Vinodhini
41	III	112416106047	VINOTH KUMAR S	S. Vinoth Kumar
42	III	112416106302	ANUSUYADEVI.K	K. Anusuyadevi
43	III	112416106304	D.W. EDWIN THILAK RAJ	D.W. Edwin
44	III	112416106305	JAYASURIYA.S	S. Jayasuriya
45	III	112416106307	KISHORE. P	P. Kishore
46	III	112416106308	MANIMEGALAI.G	G. Manimegalai
47	III	112416106310	R. SATHYARAJ	R. Sathyaraj
48	III	112416106311	SUCHITRA ROY	Suchitra Roy
49	III	112416106312	SUNITHA.B	B. Sunitha
50	III	112416106313	SUVITHA.B	B. Sunitha
51	IV	112415106001	ALICE MONCY.W	W. Alice Moncy
52	IV	112415106002	ARAVIND KUMAR.U	U. Aravind Kumar
53	IV	112415106003	BHARATH.S	S. Bharath
54	IV	112415106004	DEEBAN CHAKKARAVARATHI.P	P. Deeban
55	IV	112415106005	DIVIYA.M	M. Diviya
56	IV	112415106006	DIVYA BHARATHI.V	V. Divya Bharathi
57	IV	112415106007	ESWARI.R	R. Eswari
58	IV	112415106008	FEMILA.A	A. Femila
59	IV	112415106010	GOMATHI.P	P. Gomathi
60	IV	112415106012	IMMANUEL.D	D. Immanuel
61	IV	112415106013	ISWARYA.T	T. Iswarya
62	IV	112415106014	JANAKIRAMAN.M	M. Janaki Raman
63	IV	112415106015	KARTHICK.V	V. Karthick
64	IV	112415106016	KARTHIKEYAN.J	J. Karthikeyan
65	IV	112415106017	KAVILA.G	G. Kavila
66	IV	112415106018	KAVYA.M	M. Kavya



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S.NO	YEAR	REGISTER NUMBER	NAME OF STUDENTS	SIGNATURE
67	IV	112415106019	MANISHAMALINI.S	M. Manishamalini.S
68	IV	112415106020	NARASIMMAN.P	P. Narasimman
69	IV	112415106021	NITHISHA.S	S. Nithisha
70	IV	112415106022	PAVITHRA.S	S. Pavithra
71	IV	112415106023	PRABU.T	T. Prabu
72	IV	112415106027	RAMYA.S	S. Ramya
73	IV	112415106028	SALMA.M	M. Salma
74	IV	112415106030	SANTHOSH KUMAR.S	S. Santhosh Kumar
75	IV	112415106032	SONIYA.G	G. Soniya
76	IV	112415106034	SRIDEVI.V	V. Sridevi
77	IV	112415106035	SUBASRI.R	R. Subasri
78	IV	112415106036	SUDHI.M	M. Sudhi
79	IV	112415106039	VIGNESH KUMAR.P	P. Vignesh Kumar
80	IV	112415106301	LOKESH.R	R. Lokesh
81	IV	112415106302	NANDHA KUMAR.S	S. Nandha Kumar
82	ME-VLSI	112417419003	AMULRAJ C	C. Amulraj
83	ME-VLSI	112417419004	DAISY MONIKA M	M. Daisy Monika
84	ME-VLSI	112417419006	HANNAH JASMINE T J	J. Hannah Jasmine
85	ME-VLSI	112417419008	JAYA KUMAR J A	A. Jayakumar
86	ME-VLSI	112417419009	JAYANTHIRANI R	R. Jayanthirani
87	ME-VLSI	112417419015	PAVITHRA V	V. Pavithra
88	ME-VLSI	112417419016	SANDHIYA R	R. Sandhiya

COORDINATOR

(Mr. Anand Kumar)

HOD/ECE

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Period of the Course	18.02.2019 TO 23.02.2019

ATTENDANCE

S.N O	YEAR	REGISTER NUMBER	NAME OF STUDENTS	18.02.19		19.02.19		20.02.19		21.02.19		22.02.19		23.02.19		TOTAL HOURS (32)
				6 HOURS		6 HOURS		6 HOURS		4 HOURS		4 HOURS		5 HOURS		
				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	
1	III	112416106001	ARAVIND J	J.J	J.A	J.A	J.A	J.A	J.A	J.A	J.A	J.A	J.A	J.A	32	
2	III	112416106002	ARUNODHAYA M	A.A	A.A	A.A	A.A	A.A	A.A	A.A	A.A	A.A	A.A	A.A	32	
3	III	112416106003	ARUNTHATHI G	G.A	G.A	G.A	G.A	G.A	G.A	G.A	G.A	G.A	G.A	G.A	32	
4	III	112416106004	BALAJI S	S.B	S.B	S.B	S.B	S.B	S.B	S.B	S.B	S.B	S.B	S.B	32	
5	III	112416106005	DEEPAK KUMAR M	M.K	M.K	M.K	M.K	M.K	M.K	M.K	M.K	M.K	M.K	M.K	32	
6	III	112416106006	DILLIRANI M	M.D	M.D	M.D	M.D	M.D	M.D	M.D	M.D	M.D	M.D	M.D	32	
7	III	112416106007	EASWARI M	M.E	M.E	M.E	M.E	M.E	M.E	M.E	M.E	M.E	M.E	M.E	32	
8	III	112416106008	GAYATHRI V	V.G	V.G	V.G	V.G	V.G	V.G	V.G	V.G	V.G	V.G	V.G	32	
9	III	112416106009	GEETHA D	D.G	D.G	D.G	D.G	D.G	D.G	D.G	D.G	D.G	D.G	D.G	32	
10	III	112416106010	GOMATHI D	D.G	D.G	D.G	D.G	D.G	D.G	D.G	D.G	D.G	D.G	D.G	32	
11	III	112416106011	GREESHMA MOHAN	M.G	M.G	M.G	M.G	M.G	M.G	M.G	M.G	M.G	M.G	M.G	32	
12	III	112416106012	JAIGANESH V	V.J	V.J	V.J	V.J	V.J	V.J	V.J	V.J	V.J	V.J	V.J	32	
13	III	112416106014	KALAIVANI S	S.K	S.K	S.K	S.K	S.K	S.K	S.K	S.K	S.K	S.K	S.K	32	
14	III	112416106015	KALAIVARMAN D	D.K	D.K	D.K	D.K	D.K	D.K	D.K	D.K	D.K	D.K	D.K	32	
15	III	112416106016	KEERTHIKA G	G.K	G.K	G.K	G.K	G.K	G.K	G.K	G.K	G.K	G.K	G.K	32	
16	III	112416106018	MOURRIYA HARRISH K	K.M	K.M	K.M	K.M	K.M	K.M	K.M	K.M	K.M	K.M	K.M	32	
17	III	112416106019	NAVEN KUMAR G	G.N	G.N	G.N	G.N	G.N	G.N	G.N	G.N	G.N	G.N	G.N	32	
18	III	112416106021	PRIYA DHARSHINI G	G.P	G.P	G.P	G.P	G.P	G.P	G.P	G.P	G.P	G.P	G.P	32	
19	III	112416106022	RAKESH A	A.R	A.R	A.R	A.R	A.R	A.R	A.R	A.R	A.R	A.R	A.R	32	
20	III	112416106023	RAMYA N	N.R	N.R	N.R	N.R	N.R	N.R	N.R	N.R	N.R	N.R	N.R	32	
21	III	112416106024	RAMYA S	S.R	S.R	S.R	S.R	S.R	S.R	S.R	S.R	S.R	S.R	S.R	32	
22	III	112416106025	SANDHIYA P S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	32	
23	III	112416106026	SANDHIYA S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	S.S	32	



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S. NO	YEAR	REGISTER NUMBER	NAME OF STUDENTS	18.02.19		19.02.19		20.02.19		21.02.19		22.02.19		23.02.19		TOTAL HOURS (32)
				6 HOURS		6 HOURS		6 HOURS		4 HOURS		4 HOURS		6 HOURS		
				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	
24	III	112416106027	SANGAMITHRA V	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
25	III	112416106029	SATHYA E	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
26	III	112416106030	SENTHAMIZH SELVI R	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
27	III	112416106033	SHARAN M C	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
28	III	112416106034	SHATHIRKA R	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
29	III	112416106035	SHOBANA G	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
30	III	112416106036	SHOBANA M	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
31	III	112416106037	SRIDHAR S	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
32	III	112416106038	SRINIVASAN M	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
33	III	112416106039	SUMAN M	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
34	III	112416106040	SWAPNA R	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
35	III	112416106041	SWATHI S	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
36	III	112416106042	VANMATHI M	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
37	III	112416106043	VARALAKSHMI A	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
38	III	112416106044	VARSHA A	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
39	III	112416106045	VELU M	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
40	III	112416106046	VINODHINI V	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
41	III	112416106047	VINOTH KUMAR S	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
42	III	112416106302	ANUSUYADEVI K	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
43	III	112416106304	D.W.EDWIN THILAK RAJ	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
44	III	112416106305	JAYASURIYA.S	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
45	III	112416106307	KISHORE .P	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
46	III	112416106308	MANIMEGALAI G	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
47	III	112416106310	R.SATHYARAJ	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
48	III	112416106311	SUCHITRA ROY	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
49	III	112416106312	SUNITHA.B	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
50	III	112416106313	SUVITHA.B	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
51	IV	112415106001	ALICE MONCY W	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
52	IV	112415106002	ARAVIND KUMAR.U	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
53	IV	112415106003	BHARATH.S	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32
54	IV	112415106004	DEEBAN CHAKKARAJA RAO	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	Sn	32



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S. NO	YEAR	REGISTER NUMBER	NAME OF STUDENTS	18.02.19		19.02.19		20.02.19		21.02.19		22.02.19		23.02.19		TOTAL HOURS (32)
				6 HOURS		6 HOURS		6 HOURS		4 HOURS		4 HOURS		6 HOURS		
				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	
55	IV	112415106005	DIVIYA.M	K	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	32
56	IV	112415106006	DIVYA BHARATHI.V	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	Dm	32
57	IV	112415106007	ESWARI.R	R.P	R.P	R.P	R.P	R.P	R.P	R.P	R.P	R.P	R.P	R.P	R.P	32
58	IV	112415106008	FEMILA.A	Fm	Fm	Fm	Fm	Fm	Fm	Fm	Fm	Fm	Fm	Fm	Fm	32
59	IV	112415106010	GOMATHI.P	G	G	G	G	G	G	G	G	G	G	G	G	32
60	IV	112415106012	IMMANUEL.D	D	D	D	D	D	D	D	D	D	D	D	D	32
61	IV	112415106013	ISWARYA.T	T	T	T	T	T	T	T	T	T	T	T	T	32
62	IV	112415106014	JANAKIRAMAN.M	J	J	J	J	J	J	J	J	J	J	J	J	32
63	IV	112415106015	KARTHICK.V	V	V	V	V	V	V	V	V	V	V	V	V	32
64	IV	112415106016	KARTHIKEYAN.J	K	K	K	K	K	K	K	K	K	K	K	K	32
65	IV	112415106017	KAVILA.G	K	K	K	K	K	K	K	K	K	K	K	K	32
66	IV	112415106018	KAVYA.M	K	K	K	K	K	K	K	K	K	K	K	K	32
67	IV	112415106019	MANISHAMALINI.S	S	S	S	S	S	S	S	S	S	S	S	S	32
68	IV	112415106020	NARASIMMAN.P	N	N	N	N	N	N	N	N	N	N	N	N	32
69	IV	112415106021	NITHISHA.S	S	S	S	S	S	S	S	S	S	S	S	S	32
70	IV	112415106022	PAVITHRA.S	S	S	S	S	S	S	S	S	S	S	S	S	32
71	IV	112415106023	PRABU.T	P	P	P	P	P	P	P	P	P	P	P	P	32
72	IV	112415106027	RAMYA.S	S	S	S	S	S	S	S	S	S	S	S	S	32
73	IV	112415106028	SALMA.M	S	S	S	S	S	S	S	S	S	S	S	S	32
74	IV	112415106030	SANTHOSH KUMAR.S	S	S	S	S	S	S	S	S	S	S	S	S	32
75	IV	112415106032	SONIYA.G	S	S	S	S	S	S	S	S	S	S	S	S	32
76	IV	112415106034	SRIDEVI.V	V	V	V	V	V	V	V	V	V	V	V	V	32
77	IV	112415106035	SUBASRI.R	S	S	S	S	S	S	S	S	S	S	S	S	32
78	IV	112415106036	SUDHIL.M	S	S	S	S	S	S	S	S	S	S	S	S	32
79	IV	112415106039	VIGNESH KUMAR.P	V	V	V	V	V	V	V	V	V	V	V	V	32
80	IV	112415106301	LOKESH.R	L	L	L	L	L	L	L	L	L	L	L	L	32
81	IV	112415106302	NANDHA KUMAR.S	N	N	N	N	N	N	N	N	N	N	N	N	32
82	ME-VLSI	112417419003	AMULRAJ.C	A	A	A	A	A	A	A	A	A	A	A	A	32
83	ME-VLSI	112417419004	DAISY MONIKAM	M	M	M	M	M	M	M	M	M	M	M	M	32
84	ME-VLSI	112417419006	HANNAH JASMINE T.J	H	H	H	H	H	H	H	H	H	H	H	H	32
85	ME-VLSI	112417419008	MAYANKUMAR.J	M	M	M	M	M	M	M	M	M	M	M	M	32



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				6 HOURS		6 HOURS		6 HOURS		4 HOURS		4 HOURS		6 HOURS		
				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	
86	ME-VLSI	112417419009	JAYANTHIRANIR	Rjaya	Rjaya	Rjaya	Rjaya	Rjaya	Rjaya	Rjaya	Rjaya	Rjaya	Rjaya	Rjaya	Rjaya	32
87	ME-VLSI	112417419015	PAVITHRA V	R	R	R	R	R	R	R	R	R	R	R	R	32
88	ME-VLSI	112417419016	SANDHIYA R	R	R	R	R	R	R	R	R	R	R	R	R	32

COORDINATOR
(Mr. Anand Kumar)

HOD/ECE
(Mr. V. Rajeshkumar)



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18ECEVAC02 - "FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"

TEST QUESTION

1. What is the form of Fuzzy logic?
 - a) Two-valued logic
 - b) Crisp set logic
 - c) Many-valued logic**
 - d) Binary set logic
2. The room temperature is hot. Here the hot (use of linguistic variable is used) can be represented by _
 - a) Fuzzy Set**
 - b) Crisp Set
 - c) Fuzzy & Crisp Set
 - d) None of the mentioned
3. Fuzzy Set theory defines fuzzy operators. Choose the fuzzy operators from the following.
 - a) AND
 - b) OR
 - c) NOT
 - d) All of the mentioned**
4. Fuzzy logic is usually represented as _
 - a) IF-THEN-ELSE rules
 - b) IF-THEN rules**
 - c) Both IF-THEN-ELSE rules & IF-THEN rules
 - d) None of the mentioned
5. is/are the way/s to represent uncertainty.
 - a) Fuzzy Logic -
 - b) Probability
 - c) Entropy
 - d) All of the mentioned**
6. The less-than relation, $<$, on a set of real numbers is _
 - a) not a partial ordering because it is not asymmetric and irreflexive equals antisymmetric**
 - b) a partial ordering since it is asymmetric and reflexive
 - c) a partial ordering since it is antisymmetric and reflexive
 - d) not a partial ordering because it is not antisymmetric and reflexive
7. Power set of empty set has exactly subset.
 - a) One**
 - b) Two
 - c) Zero
 - d) Three



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8. Fuzzy logic is
- Used to respond to questions in a humanlike way
 - A new programming language used to program animation
 - The result of fuzzy thinking
 - A term that indicates logical values greater than one
9. What is the form of Fuzzy logic?
- Two -valued logic
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 - Many - valued logic**
 - Binary set logic
10. The values of the set membership is represented by
- Discrete Set
 - Degree of truth**
 - Probabilities
 - Both Degree of truth & Probabilities
11. The truth values of traditional set theory is, and that of fuzzy set is
- Either 0 or 1, between 0 & 1**
 - Between 0 & 1, either 0 or 1
 - Between 0 & 1, between 0 & 1
 - Either 0 or 1, either 0 or 1
12. Heteroassociative memory is also known as?
- unidirectional memory
 - bidirectional memory**
 - multidirectional associative memory 120/244
 - temporal associative memory
13. Which algorithm is used for solving temporal probabilistic reasoning?
- Hill - climbing search
 - Hidden Markov model**
 - Depth - first search
 - Breadth - first search
14. What are the possible values of the variable?
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 - Literals
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 - Possible states of the world
15. First Order Logic is also known as
- First Order Predicate Calculus
 - Quantification Theory
 - Lower Order Calculus
 - All of the mentioned**
16. Which is a refutation complete inference procedure for propositional logic ?
- Clauses
 - Variables
 - Propositional resolution**
 - Proposition




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17. Knowledge and reasoning also play a crucial role in dealing with environment .
- Completely Observable
 - Partially Observable**
 - Neither Completely nor Partially Observable
 - Only Completely and Partially Observable
18. Treatment chosen by doctor for a patient for a disease is based on
- Only current symptoms
 - Current symptoms plus some knowledge from the textbooks
 - Current symptoms plus some knowledge from the textbooks plus experience**
 - All of the mentioned
19. Wumpus World is a classic problem, best example of
- Single player Game
 - Two player Game 121/244
 - Reasoning with Knowledge**
 - Knowledge based Game
20. A _____ is used to demonstrate, on a purely syntactic basis, that one formula is a logical consequence of another formula.
- Deductive Systems**
 - Inductive Systems
 - Reasoning with Knowledge Based Systems
 - Search Based Systems

COORDINATOR
(Mr. Anand Kumar)

HOD/ECE
(Mr. V. Rajeshkumar)



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18ECEVAC02 - "FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"

FEEDBACK FORM

Student Reg. No. & Name: Sandhya R. 11241719016
Sem & Year: III / IV

Date: 23/02/2019

Evaluating the following Question

1. This course was interesting
a) Strongly Agree b) Agree c) Moderate d) Disagree e) Strongly Disagree
2. Was this course useful?
a) Strongly Agree b) Agree c) Moderate d) Disagree e) Strongly Disagree
3. During the course did you find any technical Difficulties?
a) Strongly Agree b) Agree c) Moderate d) Disagree e) Strongly Disagree
4. did the instructor delivered the information, you didn't know already
a) Strongly Agree b) Agree c) Moderate d) Disagree e) Strongly Disagree
5. The Subject matter was presented effectively
a) Strongly Agree b) Agree c) Moderate d) Disagree e) Strongly Disagree
6. This instructor helped me learn about how the course works in the real world.
a) Strongly Agree b) Agree c) Moderate d) Disagree e) Strongly Disagree
7. This instructor provided information I can use to be successful in my career.
a) Strongly Agree b) Agree c) Moderate d) Disagree e) Strongly Disagree
8. Overall how do you rate the Training Program?
a) Strongly Agree b) Agree c) Moderate d) Disagree e) Strongly Disagree



R. Sandhya
Student Signature
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18ECEVAC02 - "FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"

Student Reg. No. & Name: Pamyu. S. 112416106024
Sem & Year: 6 | VII

Date: 23/02/2019

19
20

1. What is the form of Fuzzy logic?
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- a) Either 0 or 1, between 0 & 1
 - b) Between 0 & 1, either 0 or 1
 - c) Between 0 & 1, between 0 & 1
 - d) Either 0 or 1, either 0 or 1
12. Heteroassociative memory is also known as?
- a) unidirectional memory
 - b) bidirectional memory
 - c) multidirectional associative memory 120/244
 - d) temporal associative memory
13. Which algorithm is used for solving temporal probabilistic reasoning?
- a) Hill - climbing search
 - b) Hidden Markov model
 - c) Depth - first search
 - d) Breadth - first search
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 - b) Literals
 - c) Discrete variable
 - d) Possible states of the world
15. First Order Logic is also known as
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 - b) Quantification Theory
 - c) Lower Order Calculus
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16. Which is a refutation complete inference procedure for propositional logic?
- a) Clauses
 - b) Variables
 - c) Propositional resolution
 - d) Proposition



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- a) Completely Observable
 - b) Partially Observable
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18ECEVAC02 - "FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES"

Course Code	18ECEVAC02
Name of the Course	FUZZY LOGIC FOR CONTROLLING THE ELECTRONICS DEVICES
Duration of the Course	32 HOURS
Academic Year	2018-19
Period of the Course	18.02.2019 to 23.02.2019

STUDENT EVALUATION SHEET

S.NO	YEAR	REGISTER NUMBER	NAME OF STUDENTS	ATTENDANCE	MARK
1	III	112416106001	ARAVIND J	32	90
2	III	112416106002	ARUNODHAYA M	32	90
3	III	112416106003	ARUNTHATHI G	32	95
4	III	112416106004	BALAJI S	32	85
5	III	112416106005	DEEPAK KUMAR M	32	85
6	III	112416106006	DILLIRANI M	32	85
7	III	112416106007	EASWARI M	32	85
8	III	112416106008	GAYATHRI V	32	90
9	III	112416106009	GEETHA D	32	90
10	III	112416106010	GOMATHI D	32	95
11	III	112416106011	GREESHMA MOHAN	32	80
12	III	112416106012	JAIGANESH V	32	80
13	III	112416106014	KALAIVANI S	32	80
14	III	112416106015	KALAIVARMAN D	32	85
15	III	112416106016	KEERTHIKA G	32	85
16	III	112416106018	MOURRIYA HARRISH K	32	85
17	III	112416106019	NAVEEN KUMAR G	32	90
18	III	112416106021	PRIYA DHARSHINI G	32	90
19	III	112416106022	RAKESH A	32	85
20	III	112416106023	RAMYA N	32	85
21	III	112416106024	RAMYA S	32	95
22	III	112416106025	SANDHIYA P S	32	85
23	III	112416106026	SANDHIYA S	32	85
24	III	112416106027	SANGAMITHRA V	32	85
25	III	112416106029	SATHYA E	32	85
26	III	112416106030	SENTHAMIZH SELVI R	32	80



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S.NO	YEAR	REGISTER NUMBER	NAME OF STUDENTS	ATTENDANCE	MARK
27	III	112416106033	SHARAN M C	32	90
28	III	112416106034	SHATHIRKA R	32	90
29	III	112416106035	SHOBANA G	32	85
30	III	112416106036	SHOBANA M	32	85
31	III	112416106037	SRIDHAR S	32	85
32	III	112416106038	SRINIVASAN M	32	85
33	III	112416106039	SUMAN M	32	90
34	III	112416106040	SWAPNA R	32	90
35	III	112416106041	SWATHI S	32	95
36	III	112416106042	VANMATHI M	32	80
37	III	112416106043	VARALAKSHMI A	32	80
38	III	112416106044	VARSHA A	32	80
39	III	112416106045	VELU M	32	85
40	III	112416106046	VINODHINI V	32	85
41	III	112416106047	VINOTH KUMAR S	32	80
42	III	112416106302	ANUSUYADEVI.K	32	80
43	III	112416106304	D.W.EDWIN THILAK RAJ	32	80
44	III	112416106305	JAYASURIYA.S	32	85
45	III	112416106307	KISHORE .P	32	85
46	III	112416106308	MANIMEGALAI.G	32	85
47	III	112416106310	R.SATHYARAJ	32	85
48	III	112416106311	SUCHITRA ROY	32	85
49	III	112416106312	SUNITHA.B	32	90
50	III	112416106313	SUVITHA.B	32	85
51	IV	112415106001	ALICE MONCY.W	32	80
52	IV	112415106002	ARAVIND KUMAR.U	32	95
53	IV	112415106003	BHARATH.S	32	90
54	IV	112415106004	DEEBAN CHAKKARAVARATHI.P	32	95
55	IV	112415106005	DIVIYA.M	32	90
56	IV	112415106006	DIVYA BHARATHI.V	32	95
57	IV	112415106007	ESWARI.R	32	90
58	IV	112415106008	FEMILA.A	32	80
59	IV	112415106010	GOMATHI.P	32	80
60	IV	112415106012	IMMANUEL.D	32	80
61	IV	112415106013	ISWARYA.T	32	85



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S.NO	YEAR	REGISTER NUMBER	NAME OF STUDENTS	ATTENDANCE	MARK
62	IV	112415106014	JANAKIRAMAN.M	32	85
63	IV	112415106015	KARTHICK.V	32	85
64	IV	112415106016	KARTHIKEYAN.J	32	80
65	IV	112415106017	KAVILA.G	32	80
66	IV	112415106018	KAVYA.M	32	85
67	IV	112415106019	MANISHAMALINI.S	32	85
68	IV	112415106020	NARASIMMAN.P	32	90
69	IV	112415106021	NITHISHA.S	32	90
70	IV	112415106022	PAVITHRA.S	32	95
71	IV	112415106023	PRABU.T	32	80
72	IV	112415106027	RAMYA.S	32	80
73	IV	112415106028	SALMA.M	32	80
74	IV	112415106030	SANTHOSH KUMAR.S	32	85
75	IV	112415106032	SONIYA.G	32	85
76	IV	112415106034	SRIDEVI.V	32	85
77	IV	112415106035	SUBASRI.R	32	80
78	IV	112415106036	SUDHIL.M	32	80
79	IV	112415106039	VIGNESH KUMAR.P	32	85
80	IV	112415106301	LOKESH.R	32	85
81	IV	112415106302	NANDHA KUMAR.S	32	80
82	ME-VLSI	112417419003	AMULRAJ C	32	80
83	ME-VLSI	112417419004	DAISY MONIKA M	32	80
84	ME-VLSI	112417419006	HANNAH JASMINE T J	32	80
85	ME-VLSI	112417419008	JAYA KUMAR J A	32	90
86	ME-VLSI	112417419009	JAYANTHIRANI R	32	95
87	ME-VLSI	112417419015	PAVITHRA V	32	90
88	ME-VLSI	112417419016	SANDHIYA R	32	80

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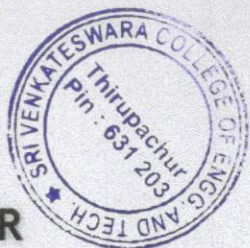
This is to certify that Mr/Ms **SUBASRI R. (112415106035)**..... UG/PG

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DEVICES' held from 18.02.2019 to 23.02.2019

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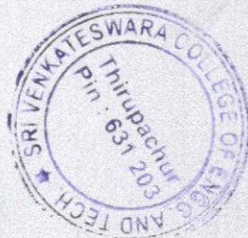
This is to certify that Mr/Ms **JANAKIRAMAN..M.(1124510604)**.. UG/PG

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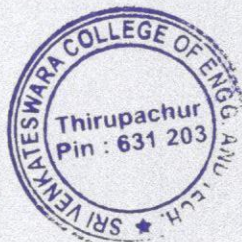
This is to certify that Mr/Ms **NARASIMMAN P (112415106020)** UG/PG

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This is to certify that Mr/Ms **PAVITHRA V (112417419015)**..... UG/PG

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This is to certify that Mr/Ms **BHARATH.S (112415106003)**..... UG/PG

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This is to certify that Mr/Ms **RAMYA S (112416106024)**..... UG/PG

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